

16

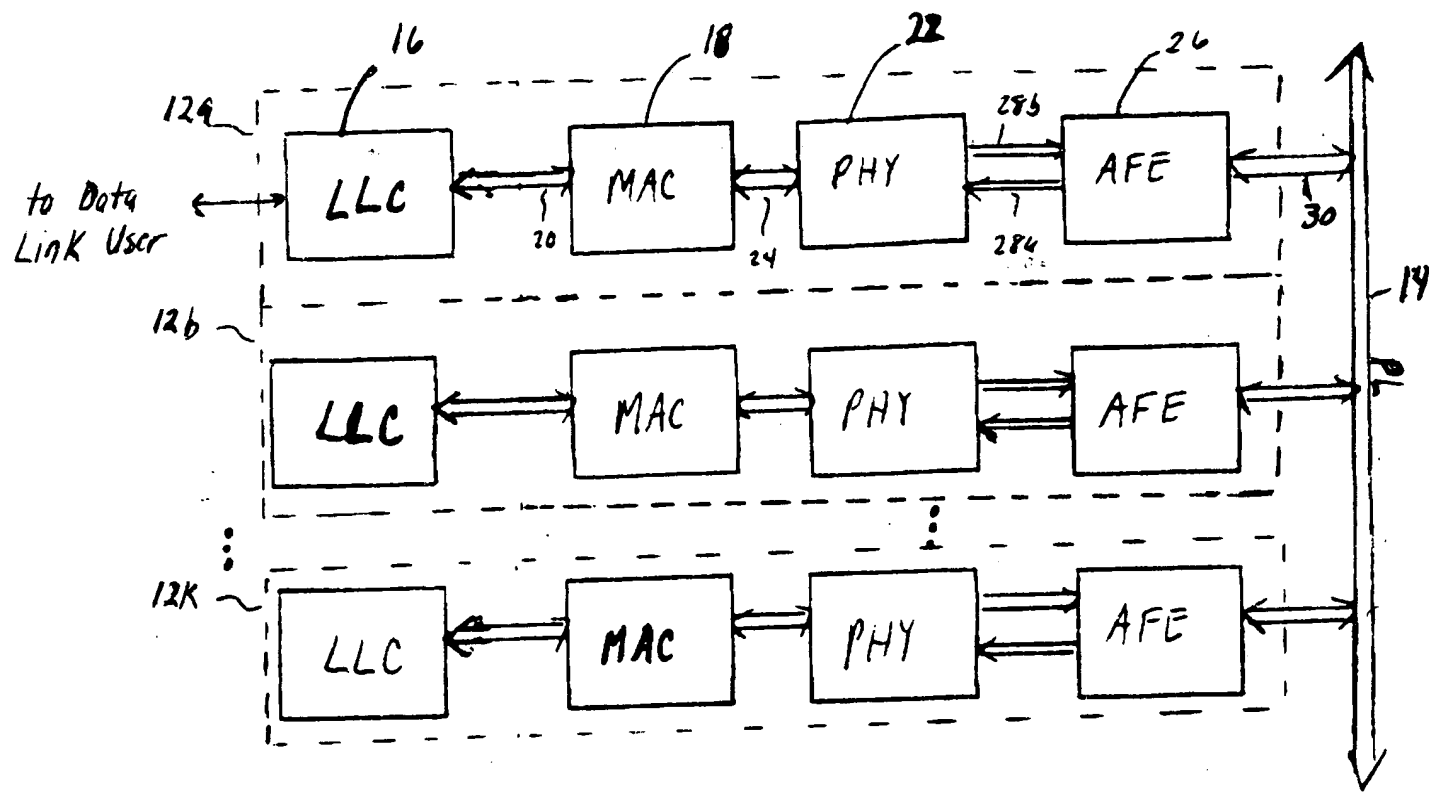


FIG. 1

004000-2692900

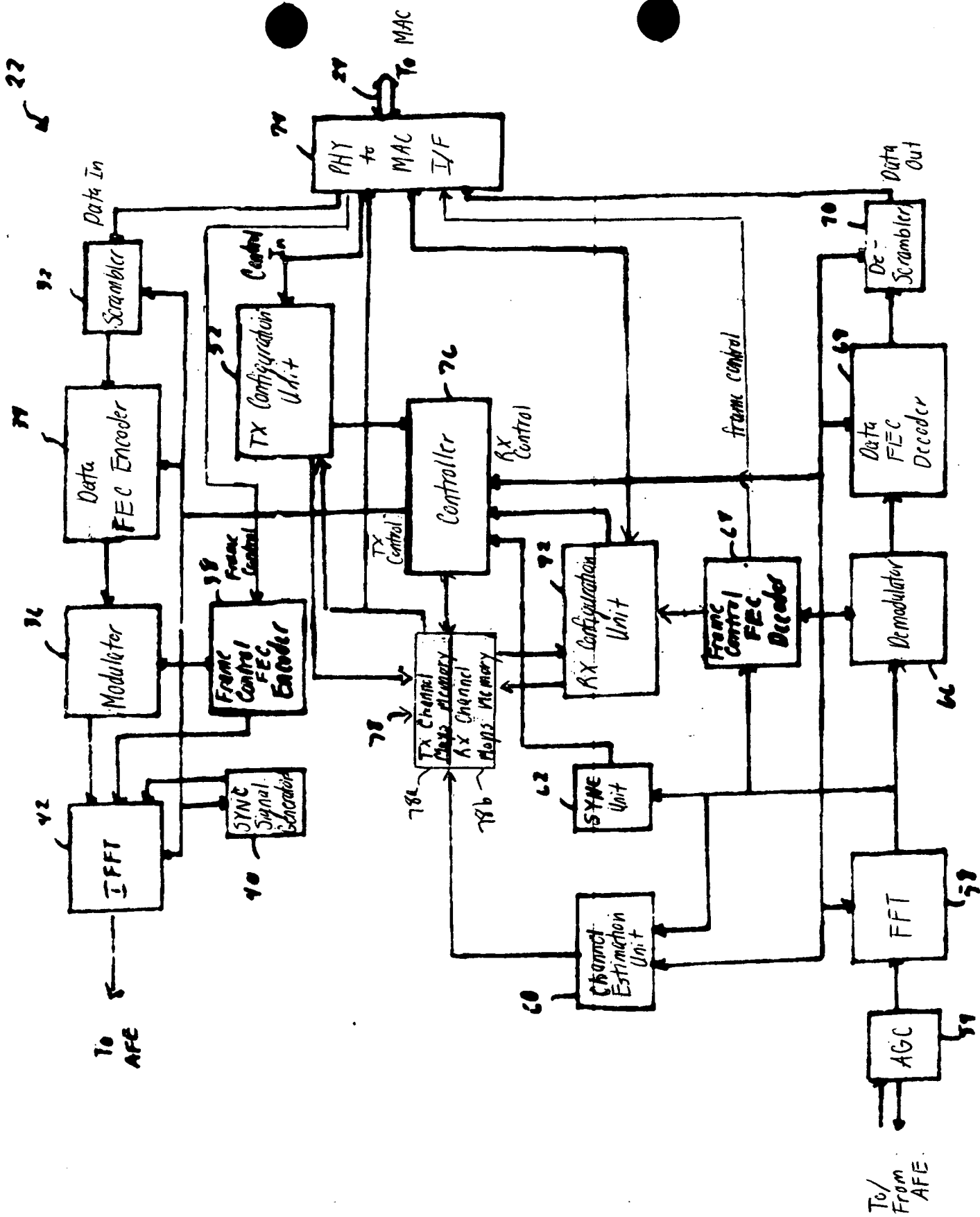


FIG. 2

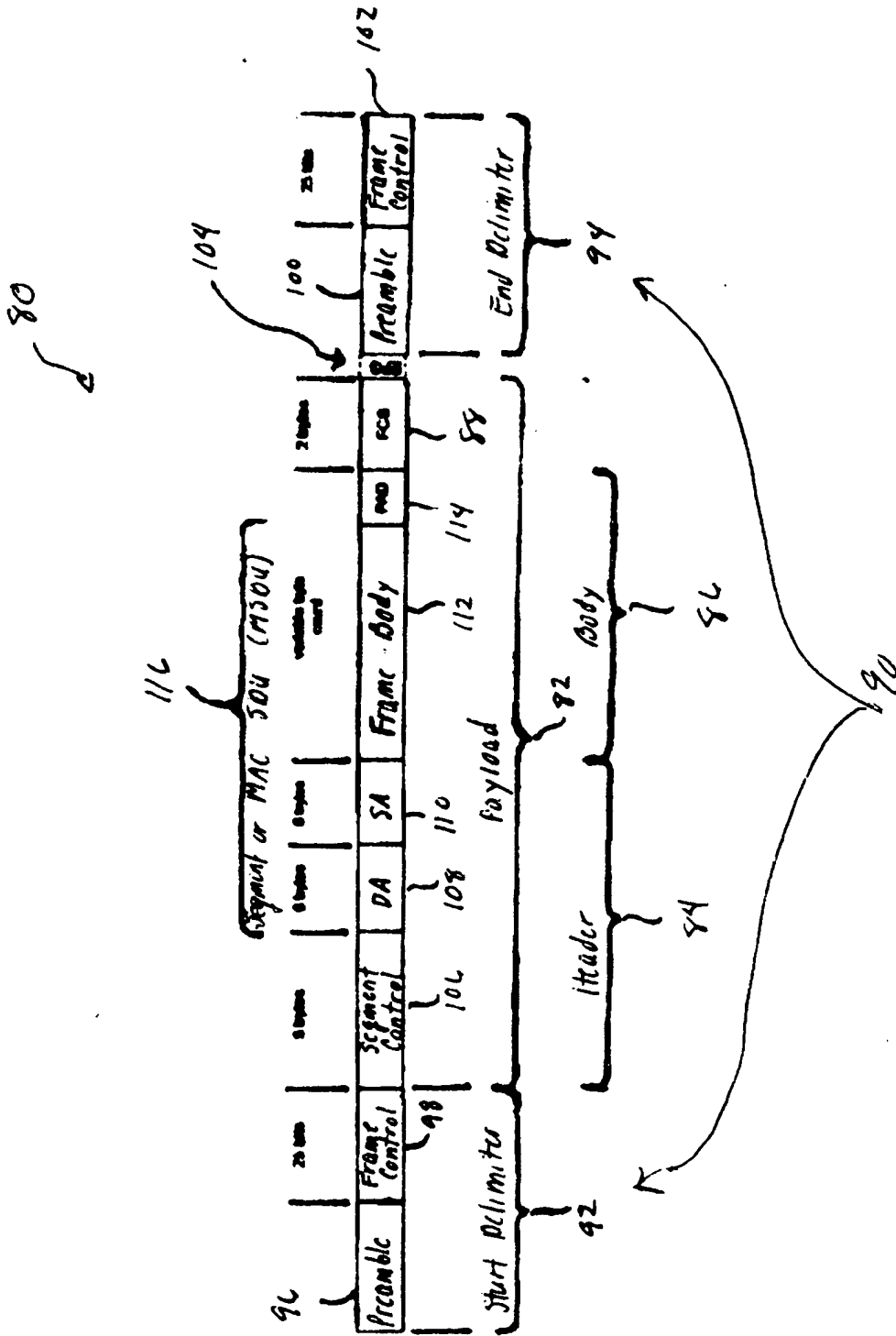


FIG. 3

120 →

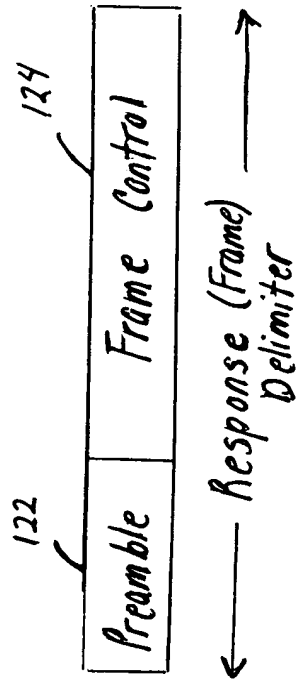


FIG. 4

CC		DT	VF	FCCS
130	132		134	136

FIG. 5A

102

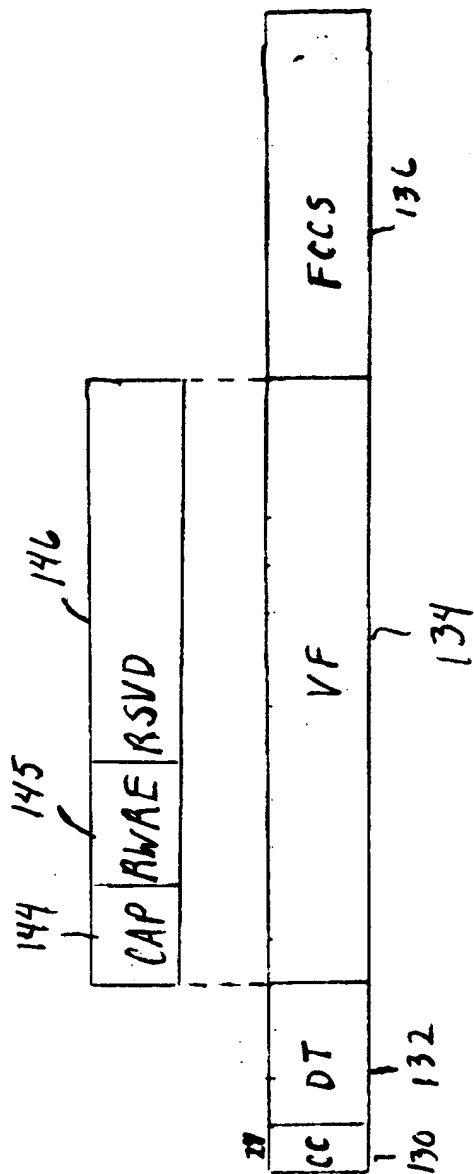


FIG 5B

Diagram illustrating a packet structure with fields and bit positions:

- FTPE** (Field Type) at bit 149.
- RSVD** (Reserved) at bit 150.
- RFCS** (Reserved for Future Use) at bit 148.
- CAP** (Control) at bit 147.
- ACK** (Acknowledgment) at bit 146.
- AFF** (Affirmation) at bit 145.
- VF** (Variable) at bit 144.
- DT** (Data Type) at bit 143.
- FCCS** (Field Control) at bit 142.

Bit positions 149, 150, 148, 147, 146, 145, 144, 143, 142, 141, 140, 139, 138, 137, 136, 135, 134, 133, 132, 131, 130, 129, 128, 127, 126, 125, 124 are indicated along the top of the packet structure.

6

→ 106

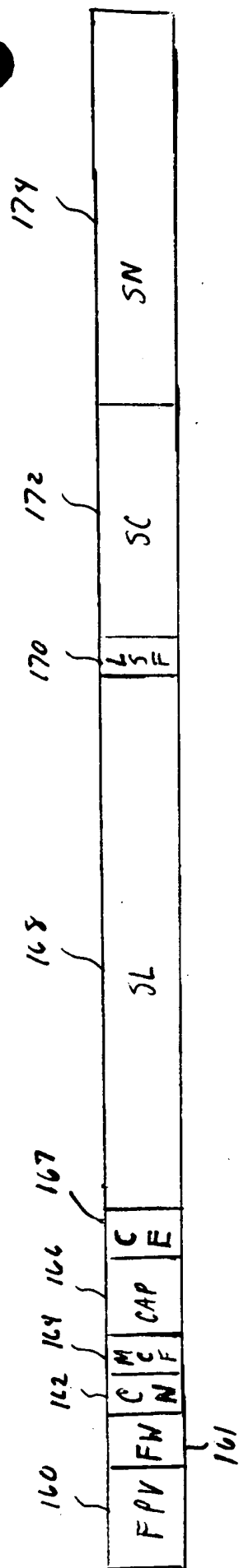


FIG. 7

112

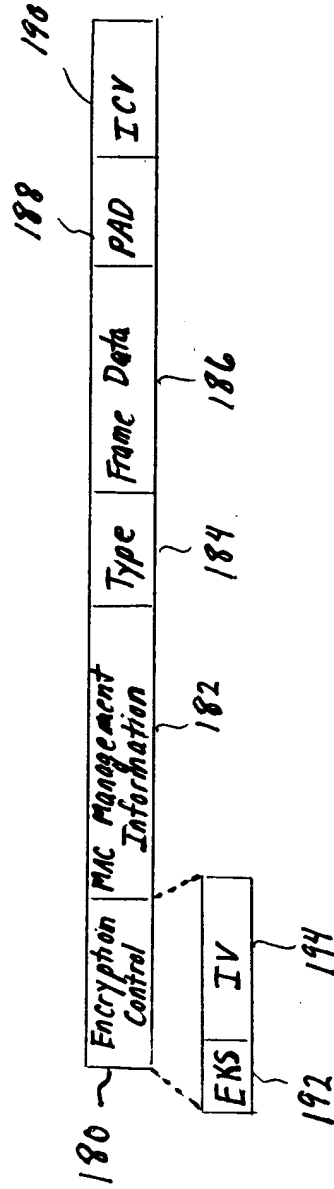


FIG. 8

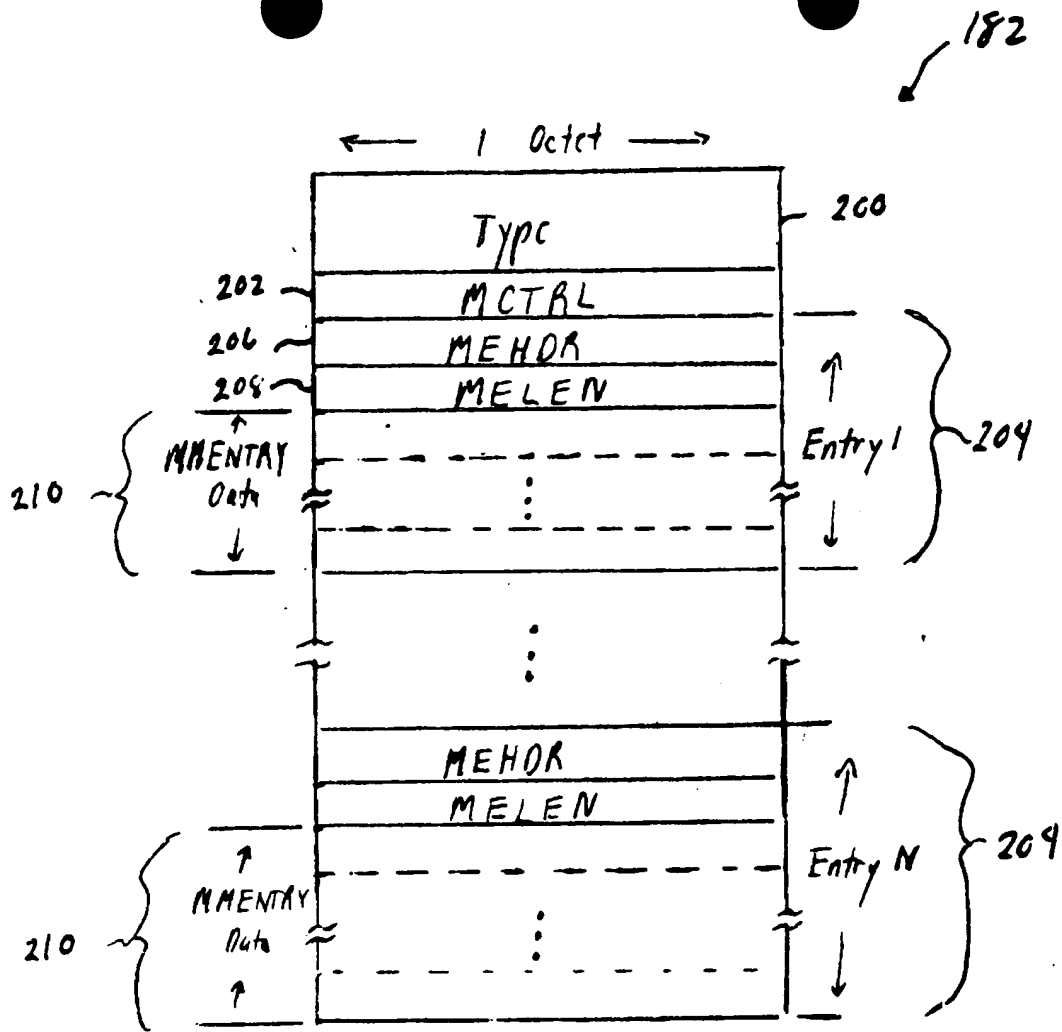


FIG. 1

Diagram of a data structure 202. It consists of a rectangular block divided into two sections. The left section is labeled 212 and contains the text 'ASVD'. The right section is labeled 219 and contains the text 'NE'. A bracket on the right side of the block is labeled 202.

FIG. 10



FIG. 11

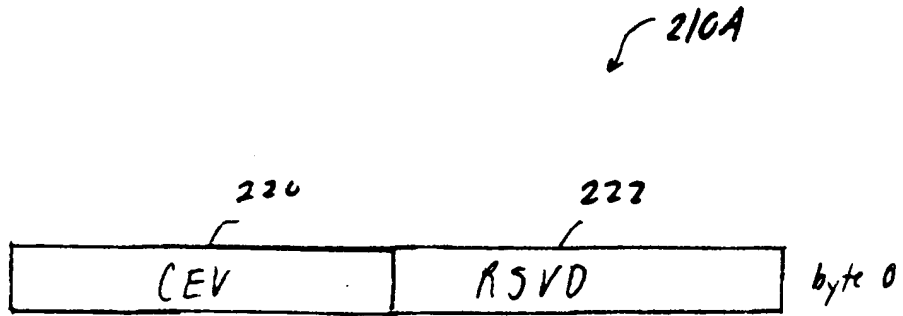


FIG. 12A

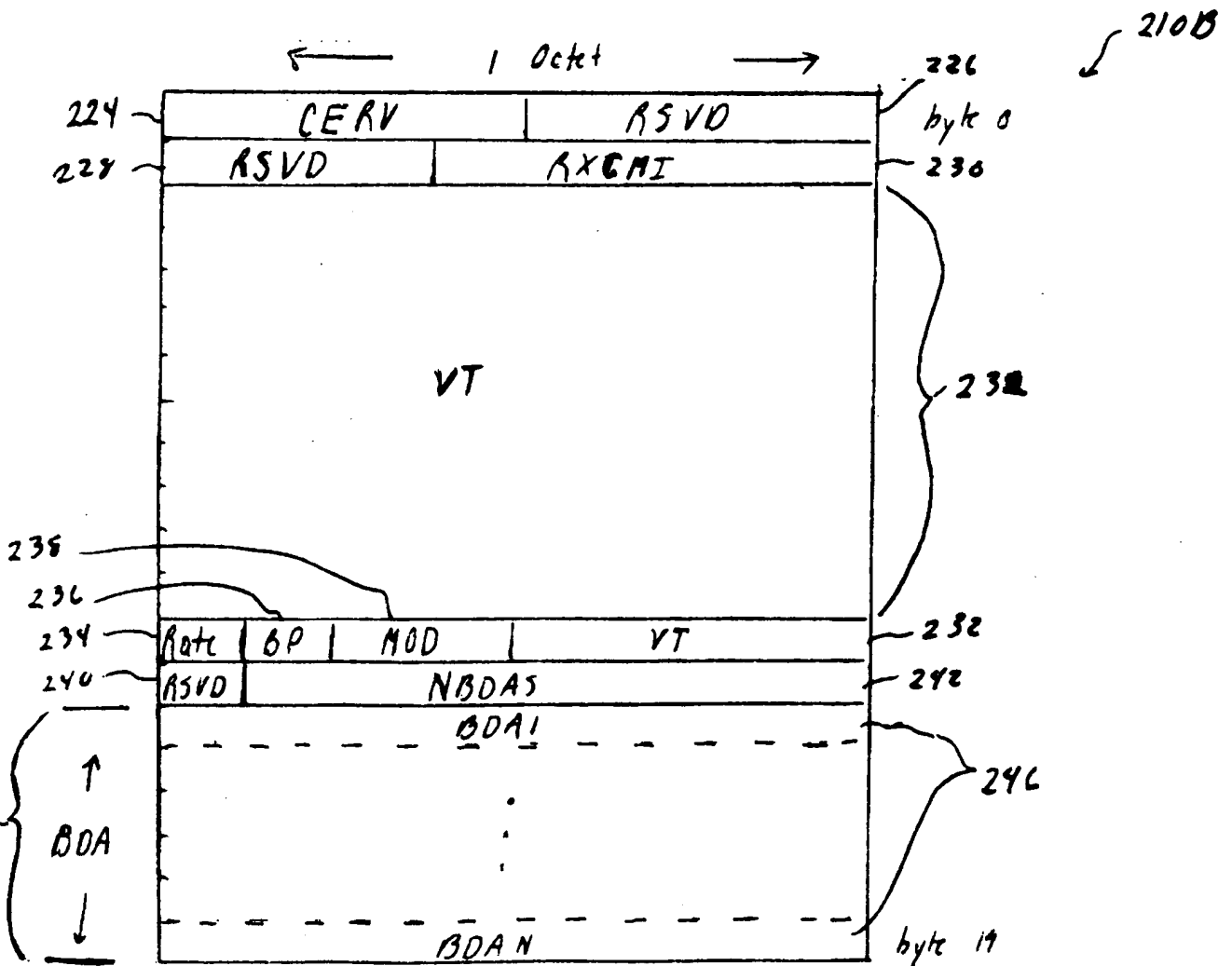


FIG. 12B

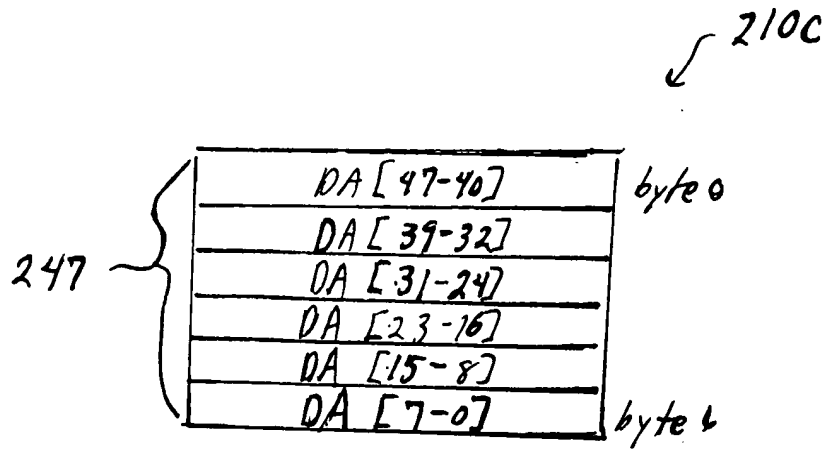


FIG. 13A

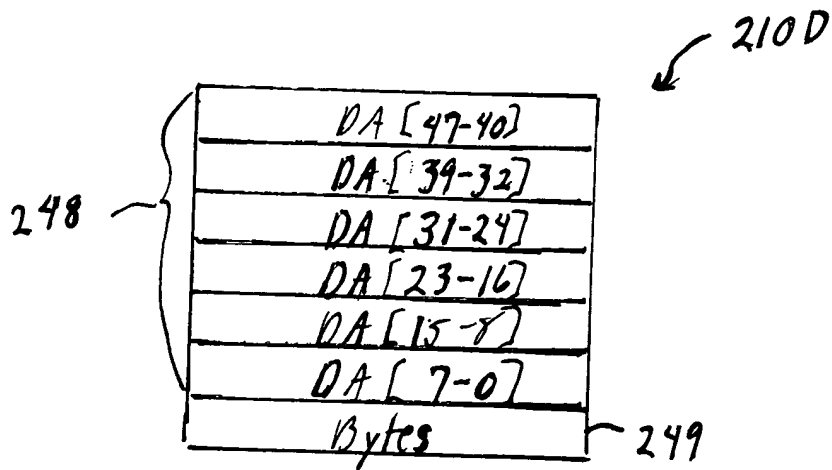


FIG. 13B

210 E

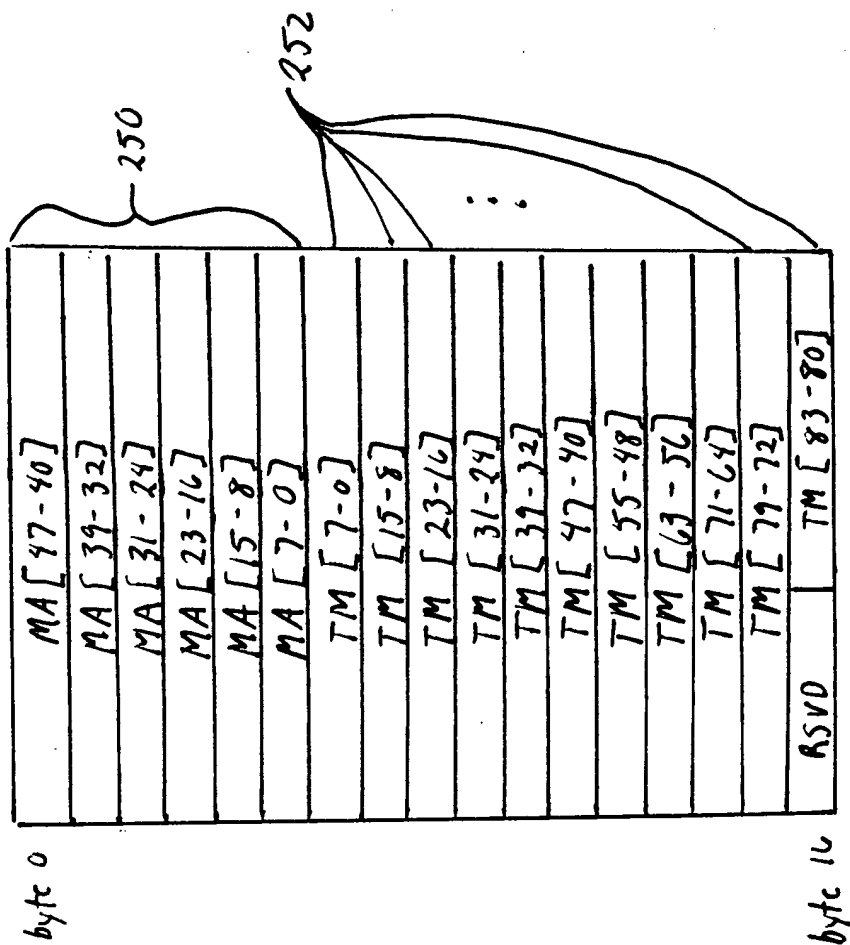


FIG. 14

210F

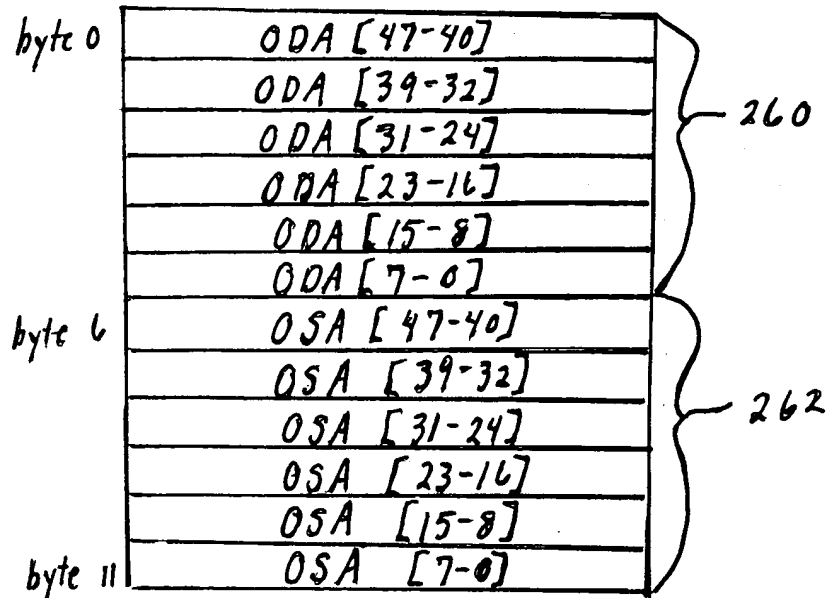


FIG. 15

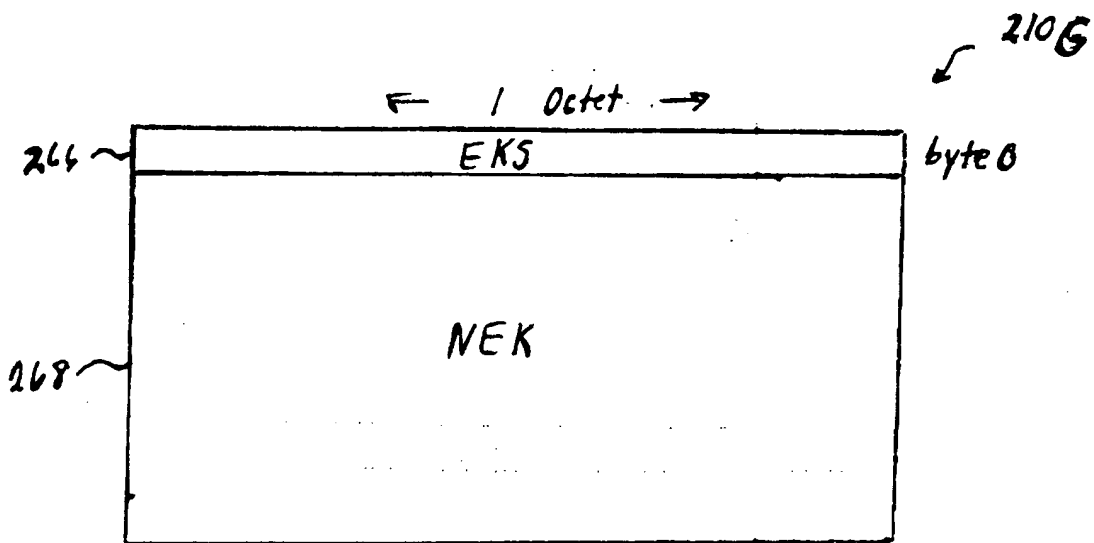


FIG. 16

—

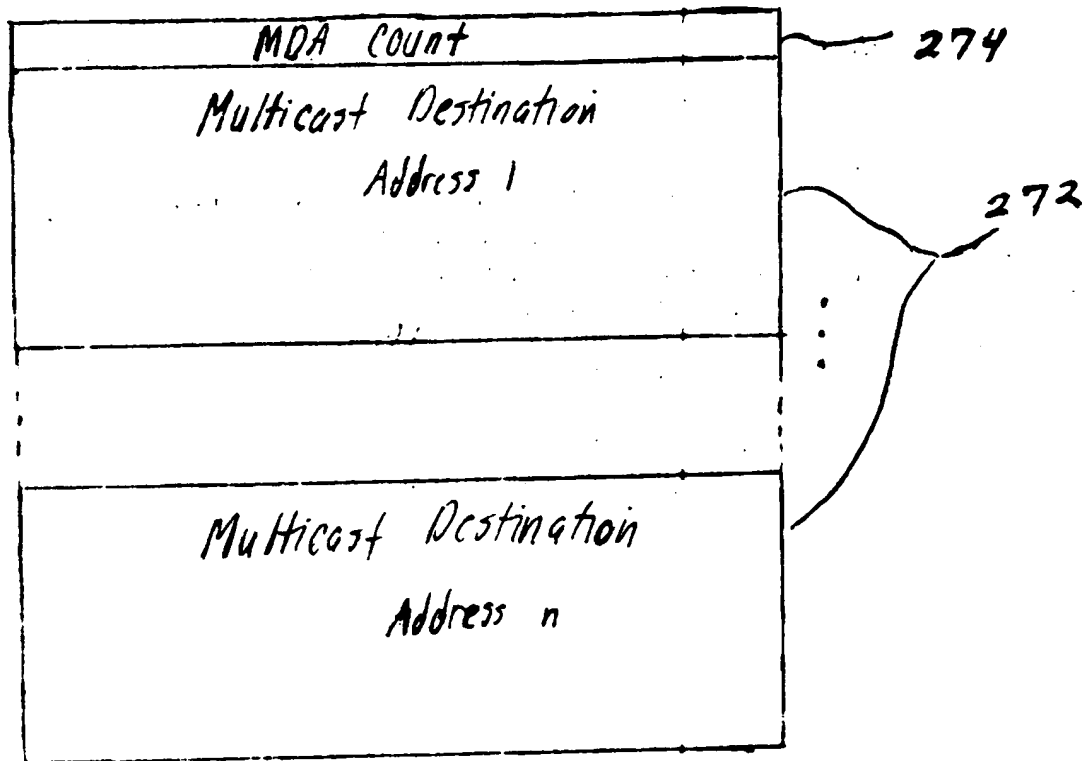


FIG. 17

2025 RELEASE UNDER E.O. 14176

210I

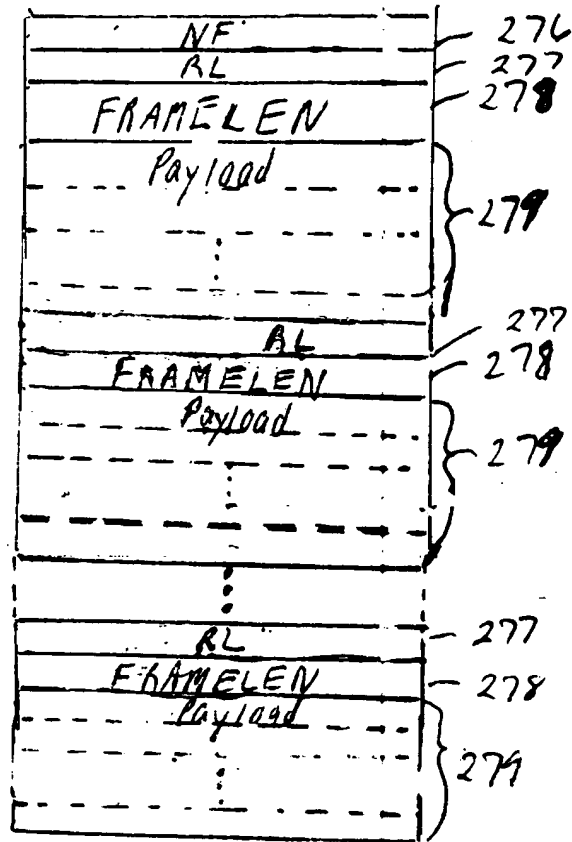


FIG. 18

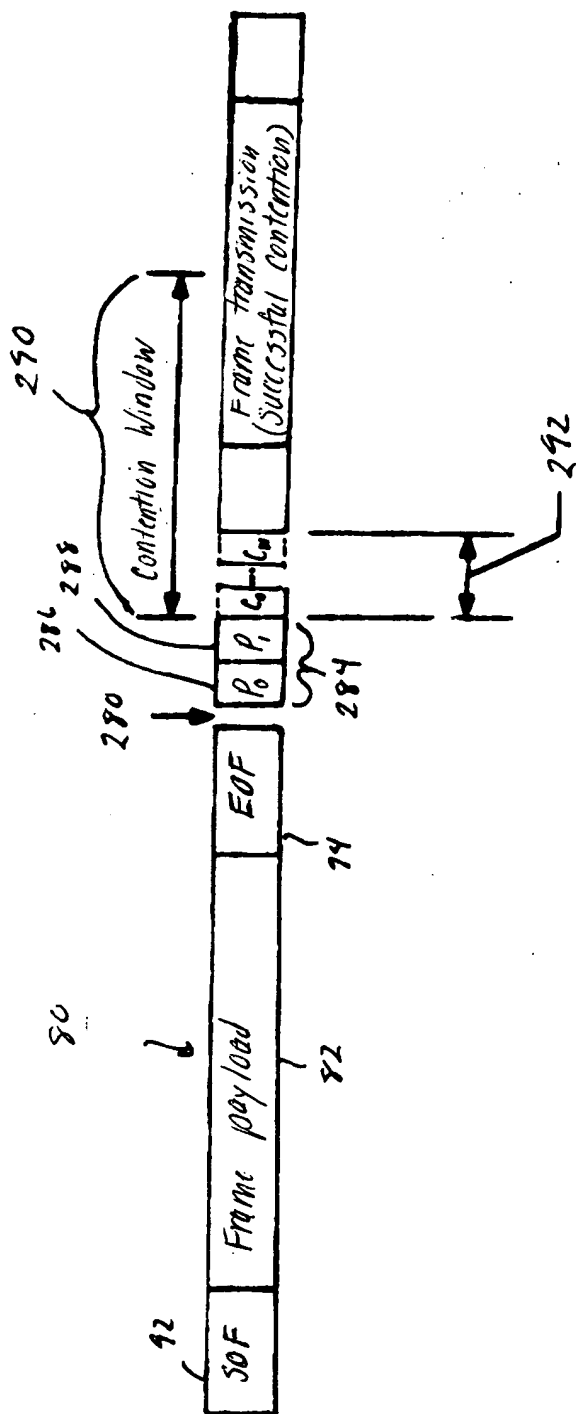


FIG. 19A

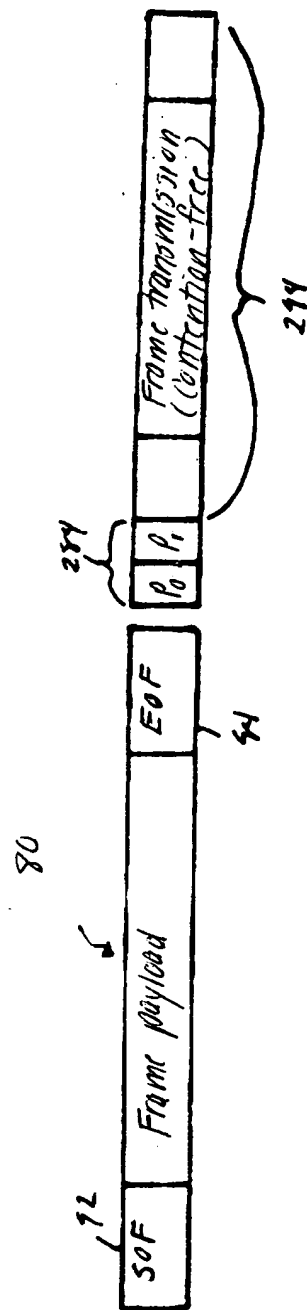


FIG. 19B

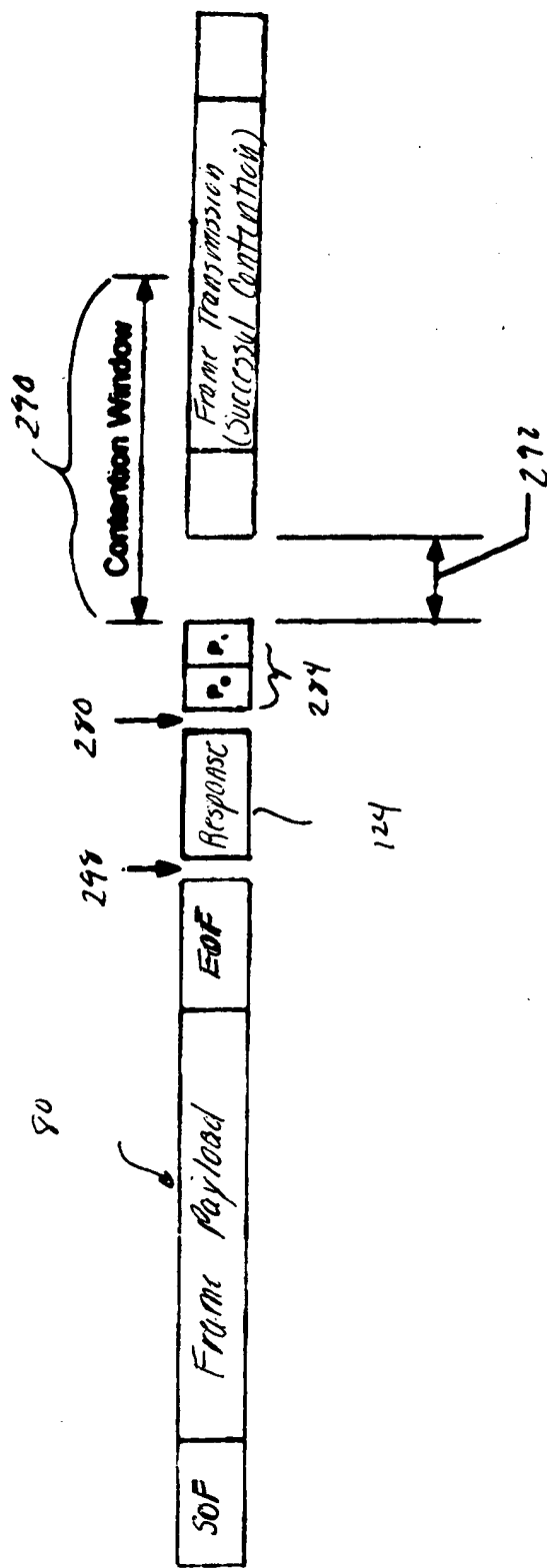


FIG. 19E

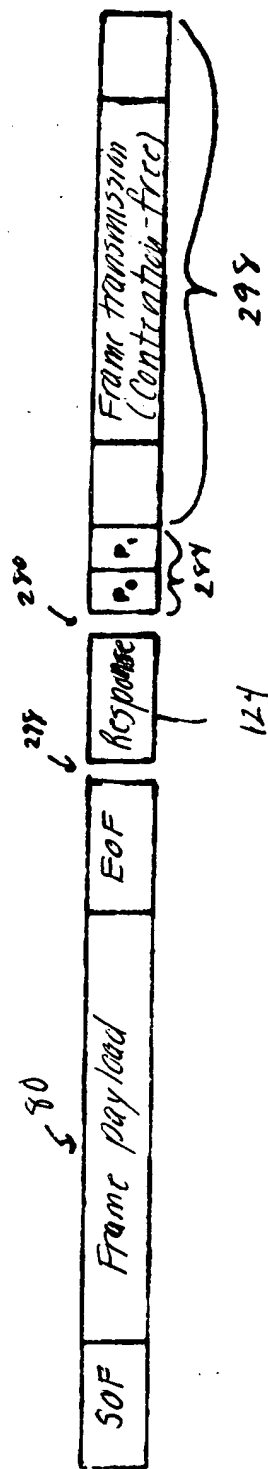
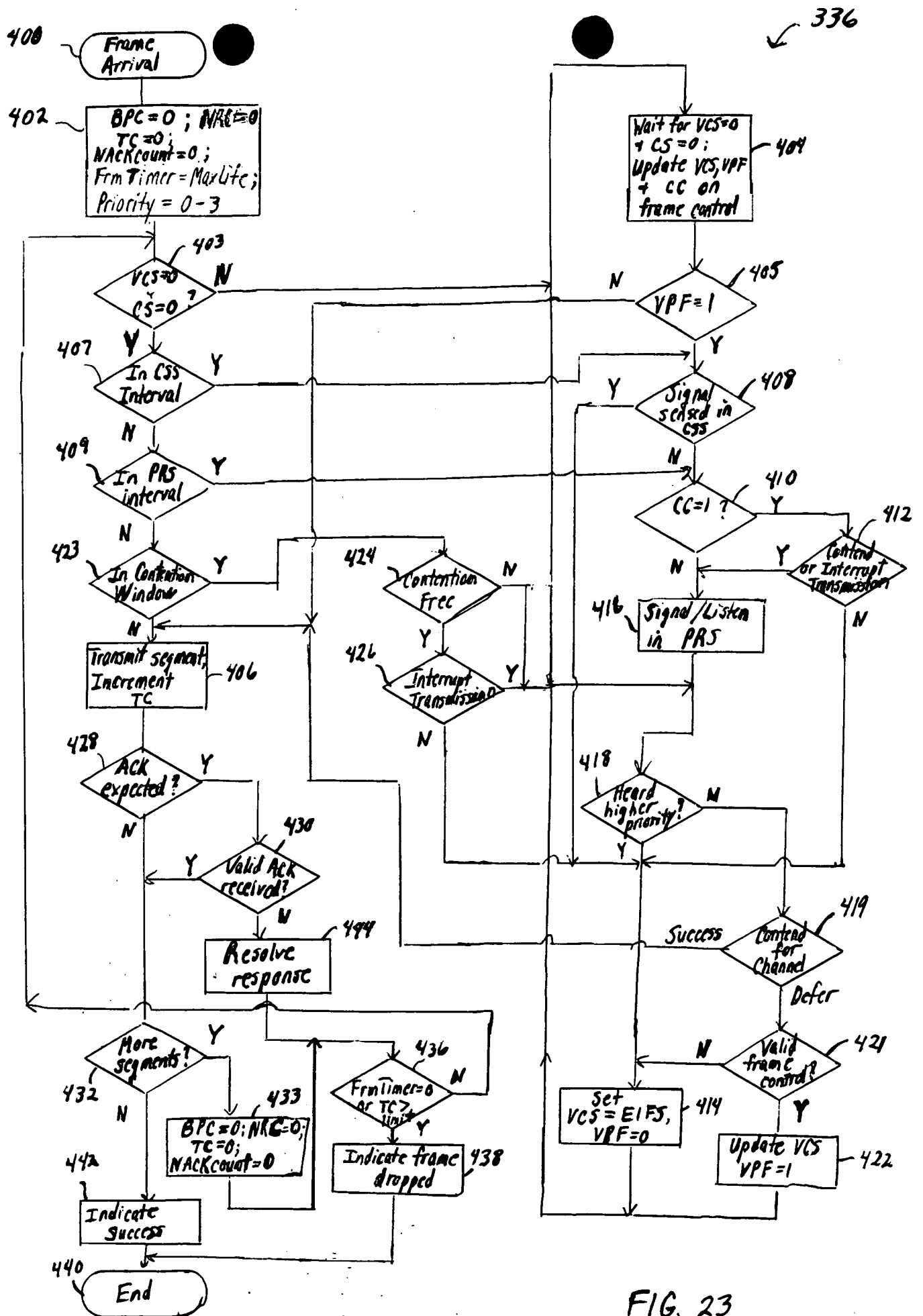


FIG. 19D



from Step 430 (FIG. 23)

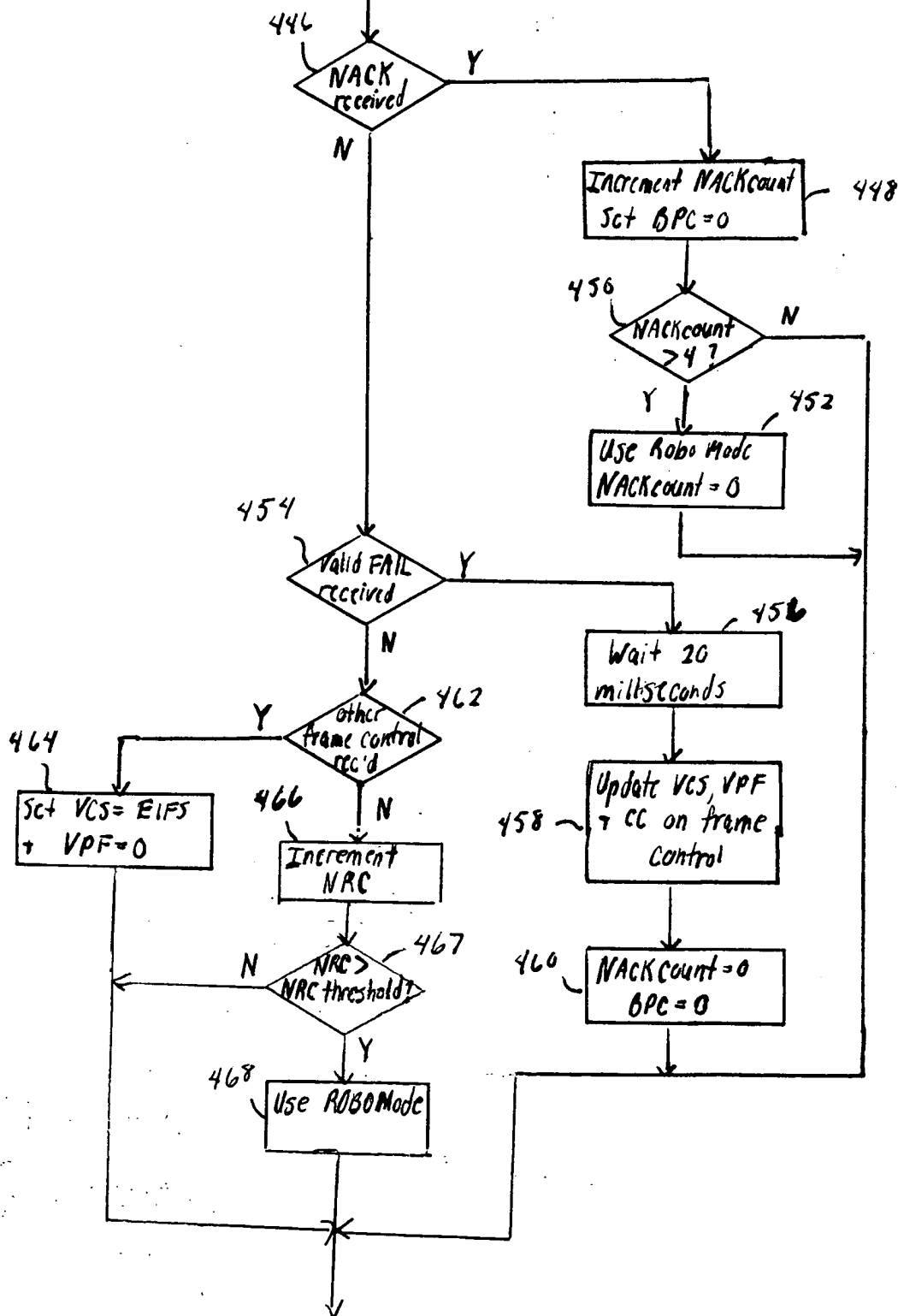


FIG. 24

From Step 418 (FIG. 23)

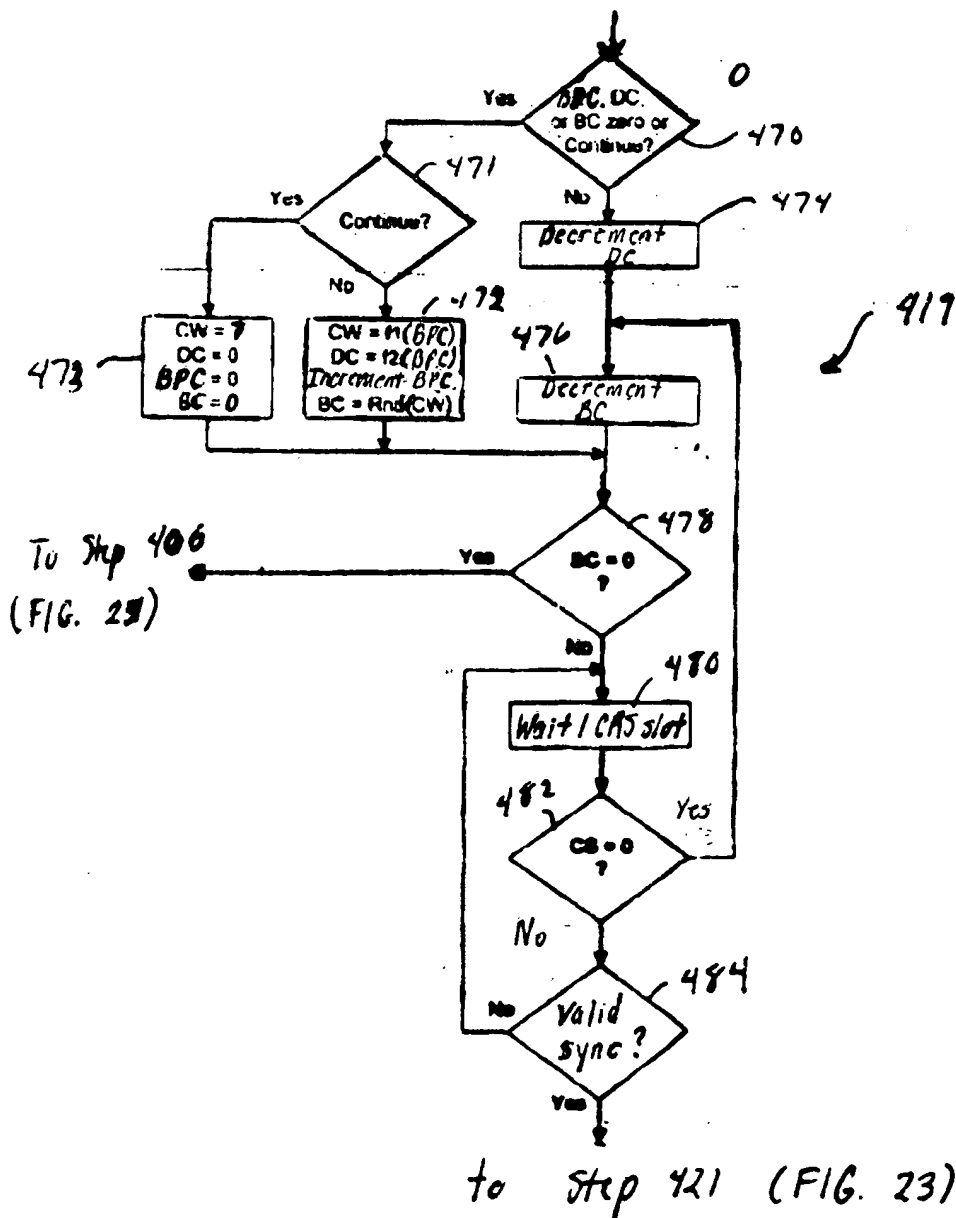


FIG. 25

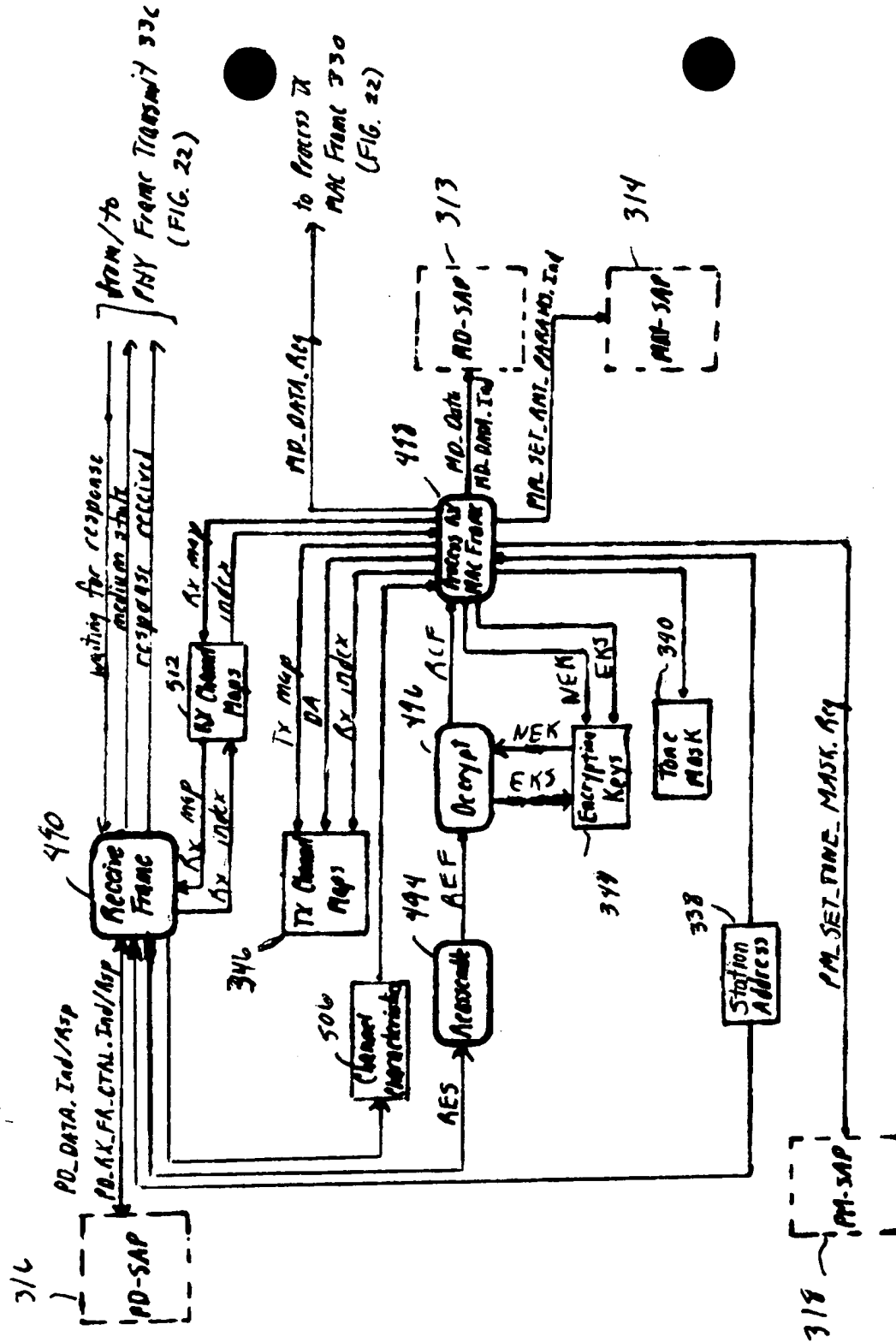
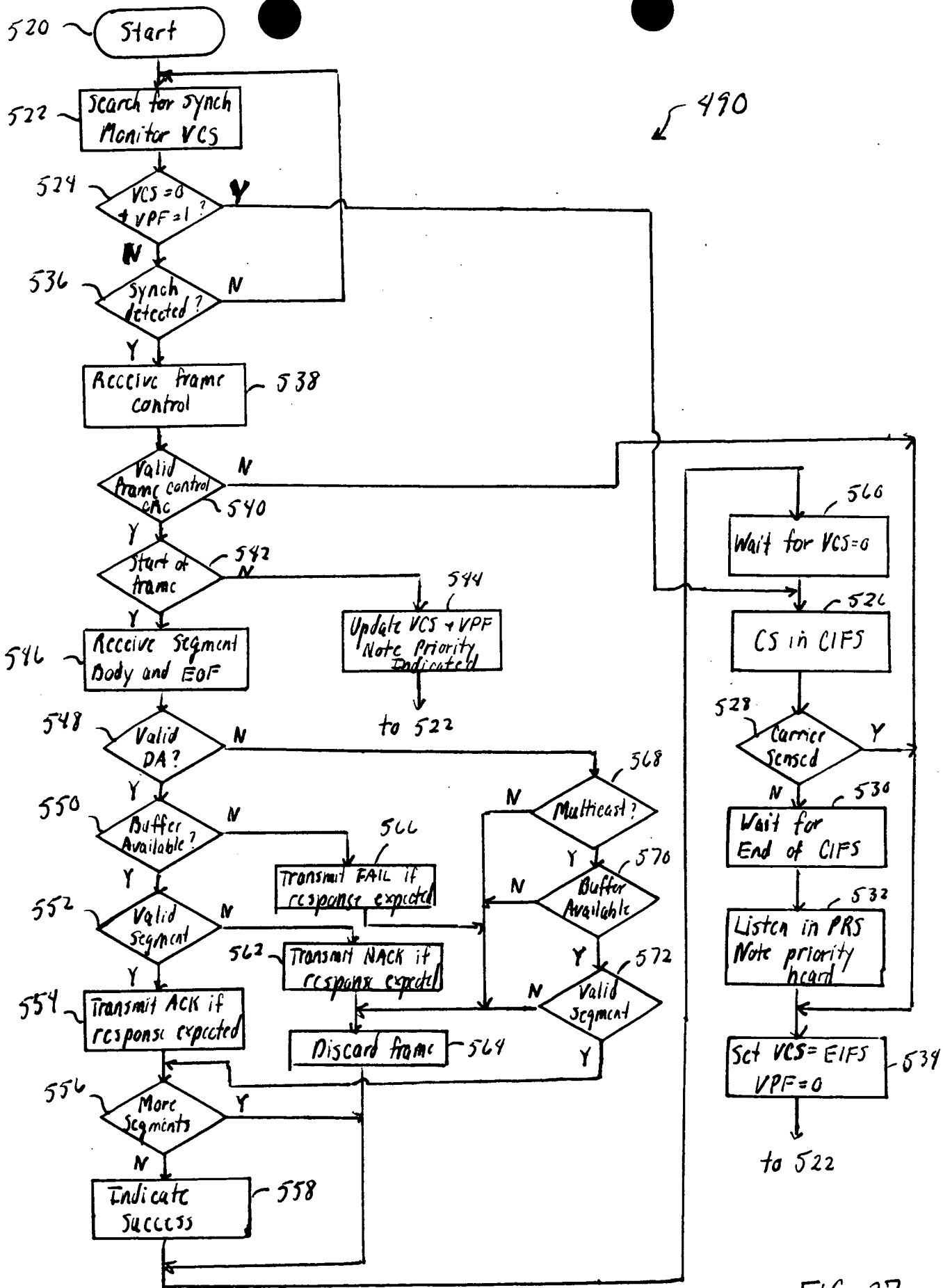
[illegible]

FIG. 26



575

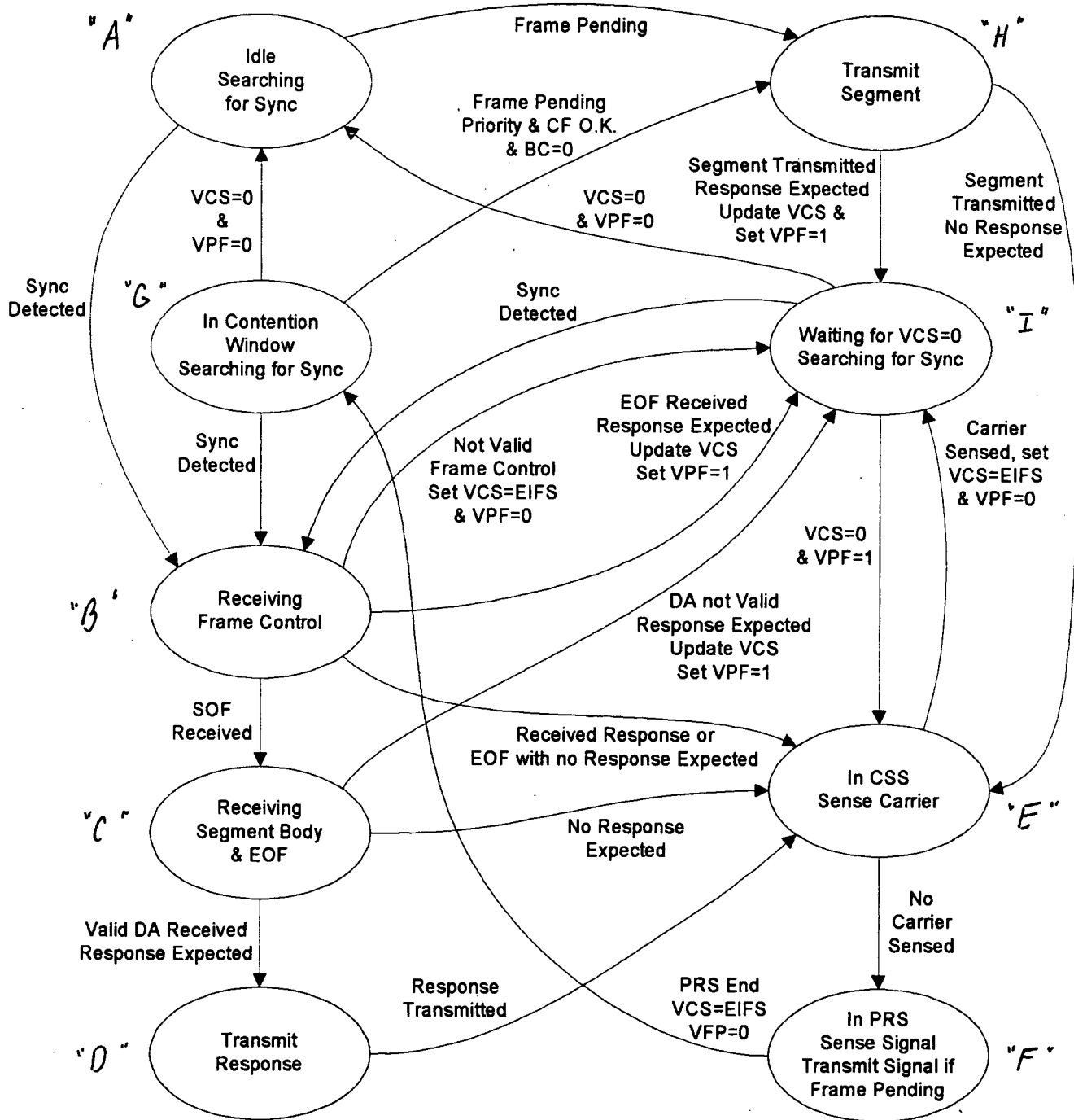


FIG. 28

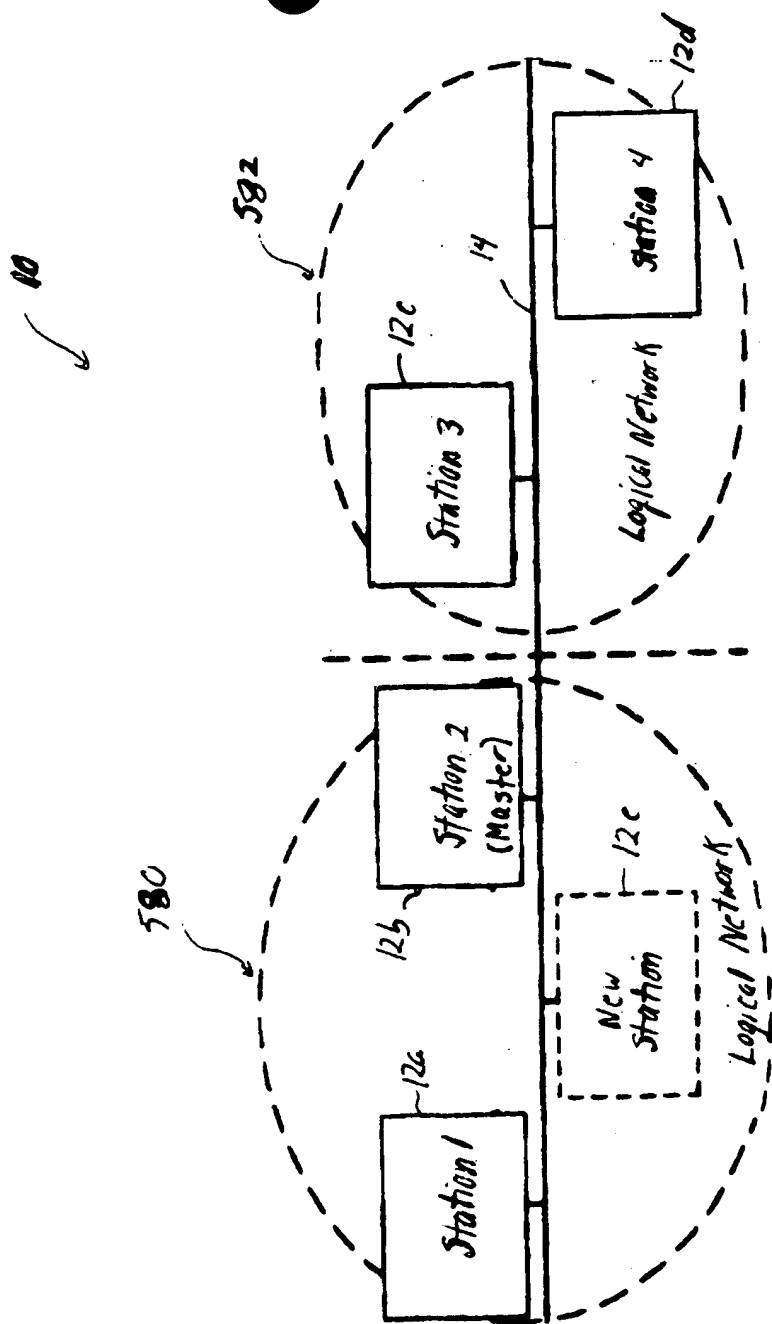


FIG. 29

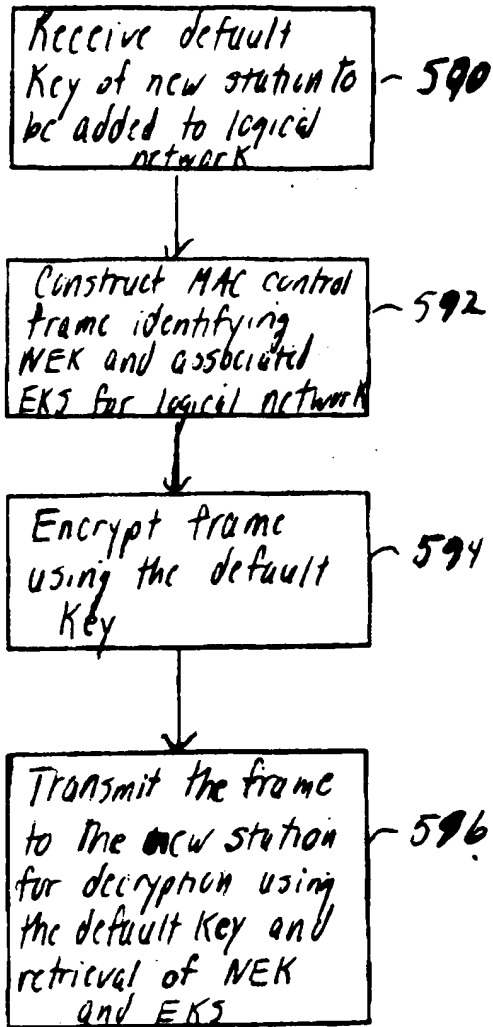


FIG.

580

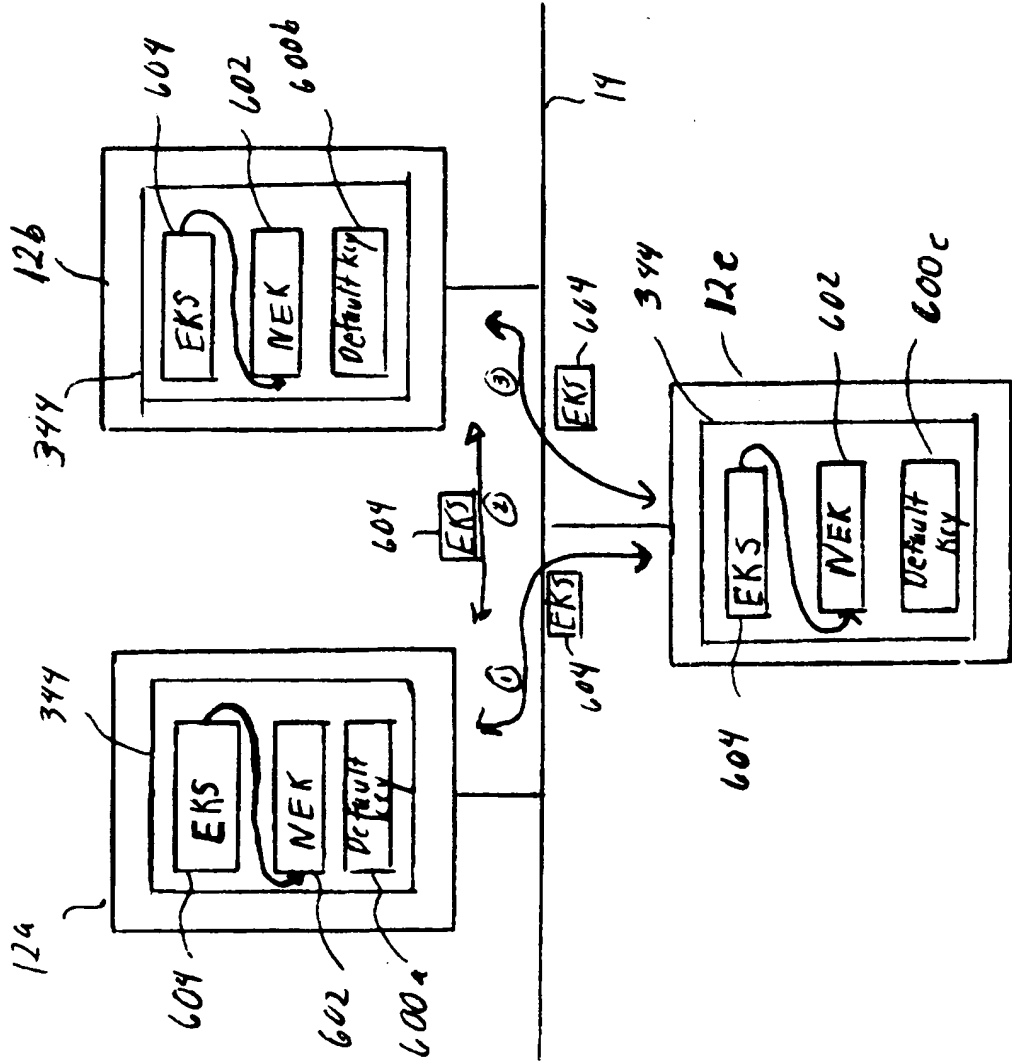


FIG. 31

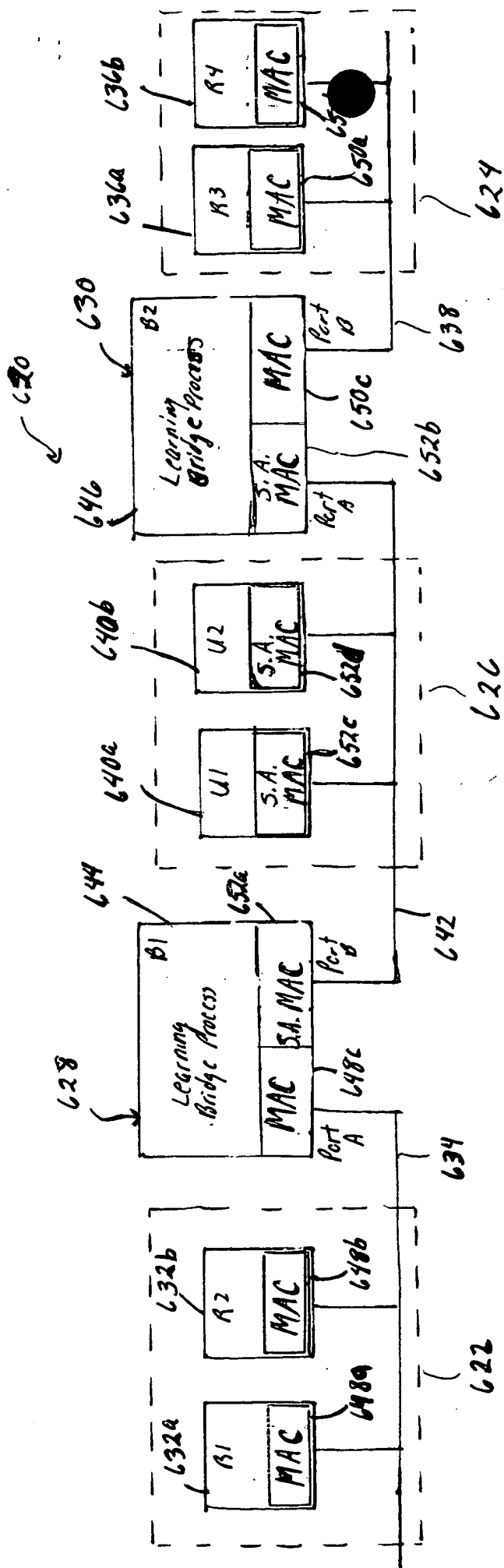
[illegible]

FIG. 32

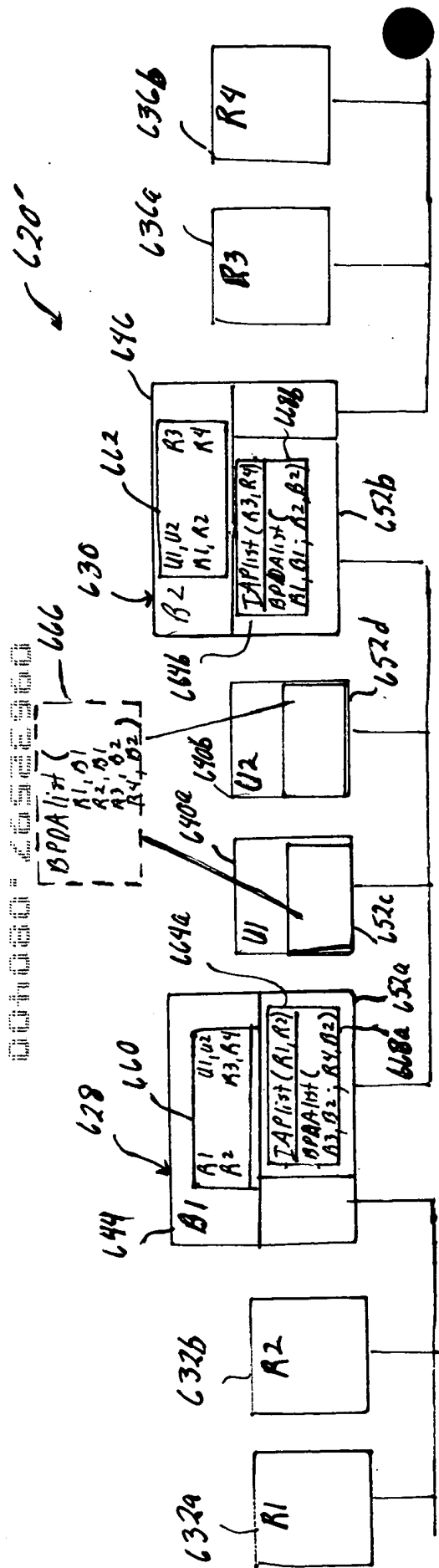


FIG. 33

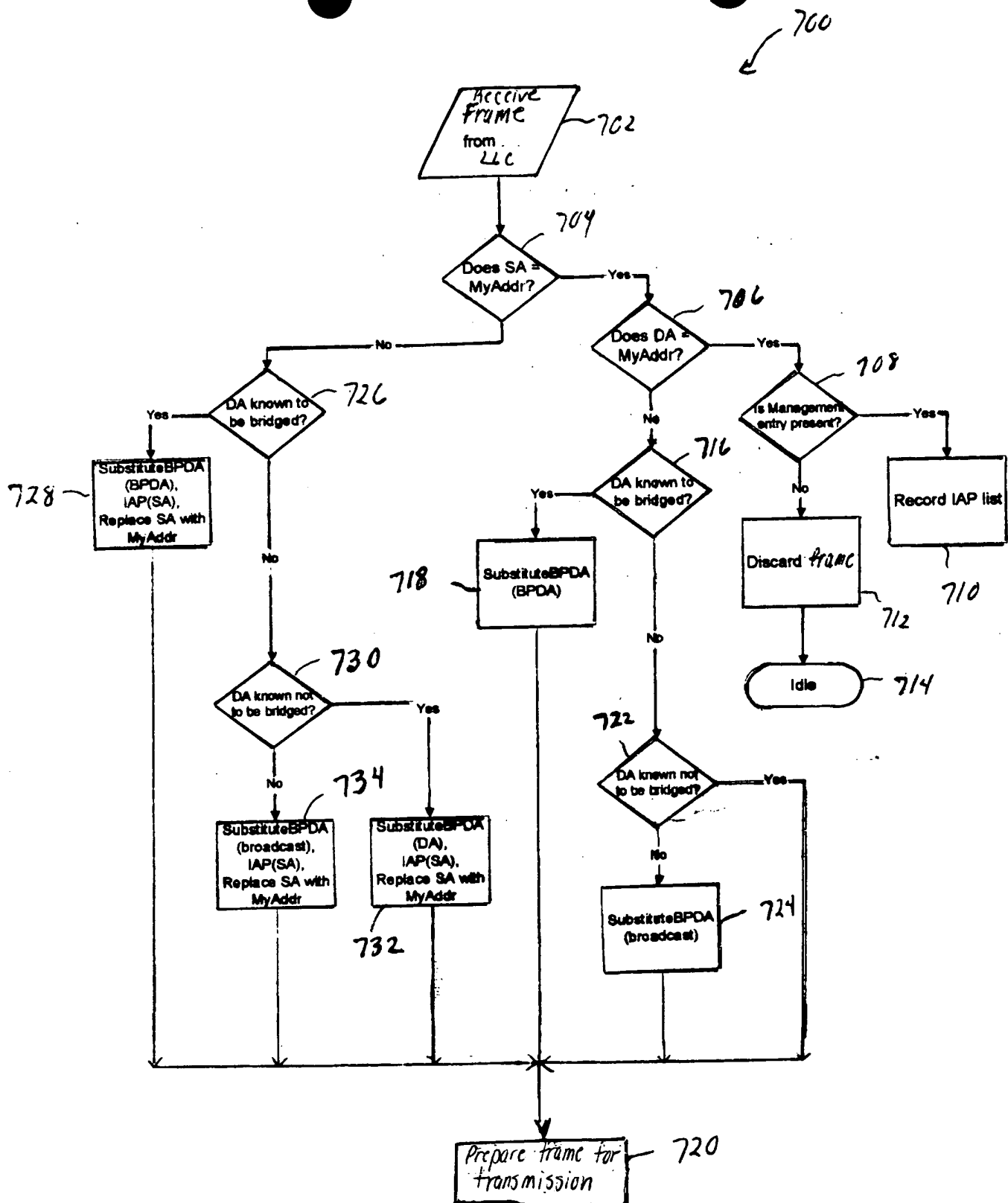


FIG. 34

from FIG. 34

720

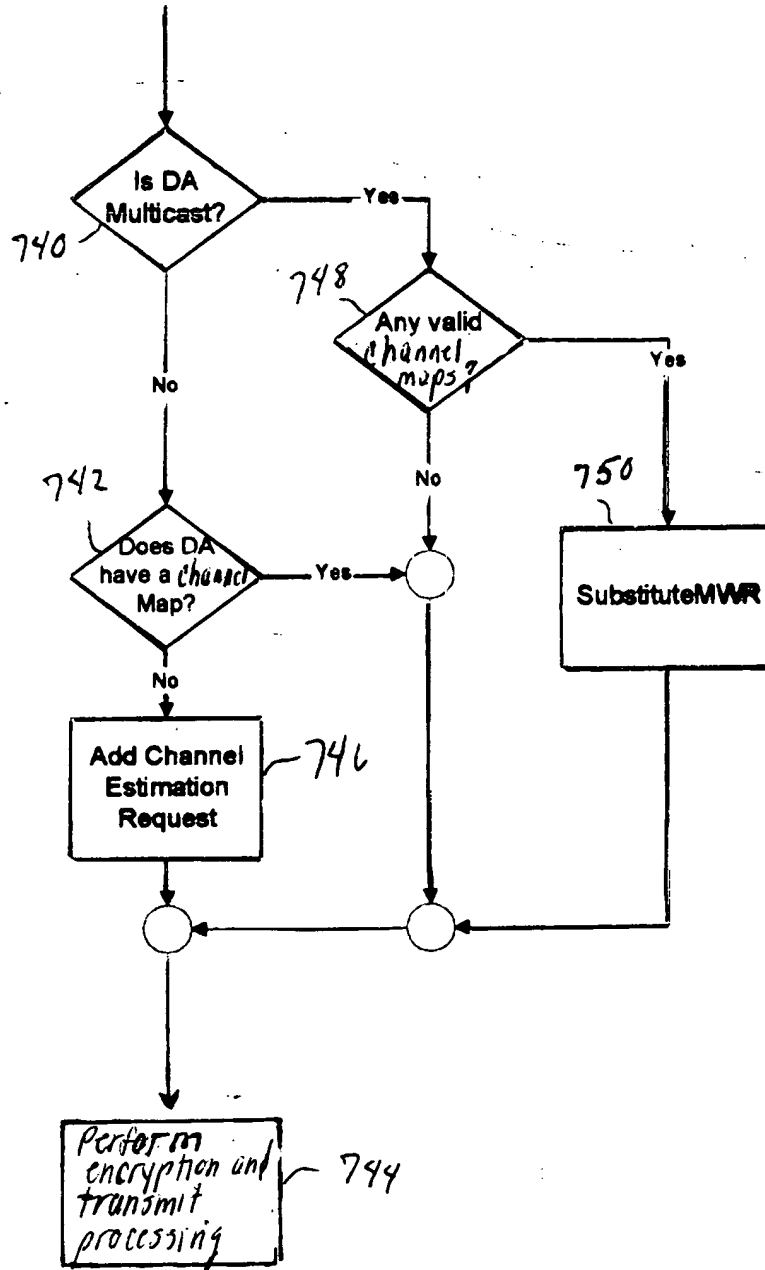


FIG. 35

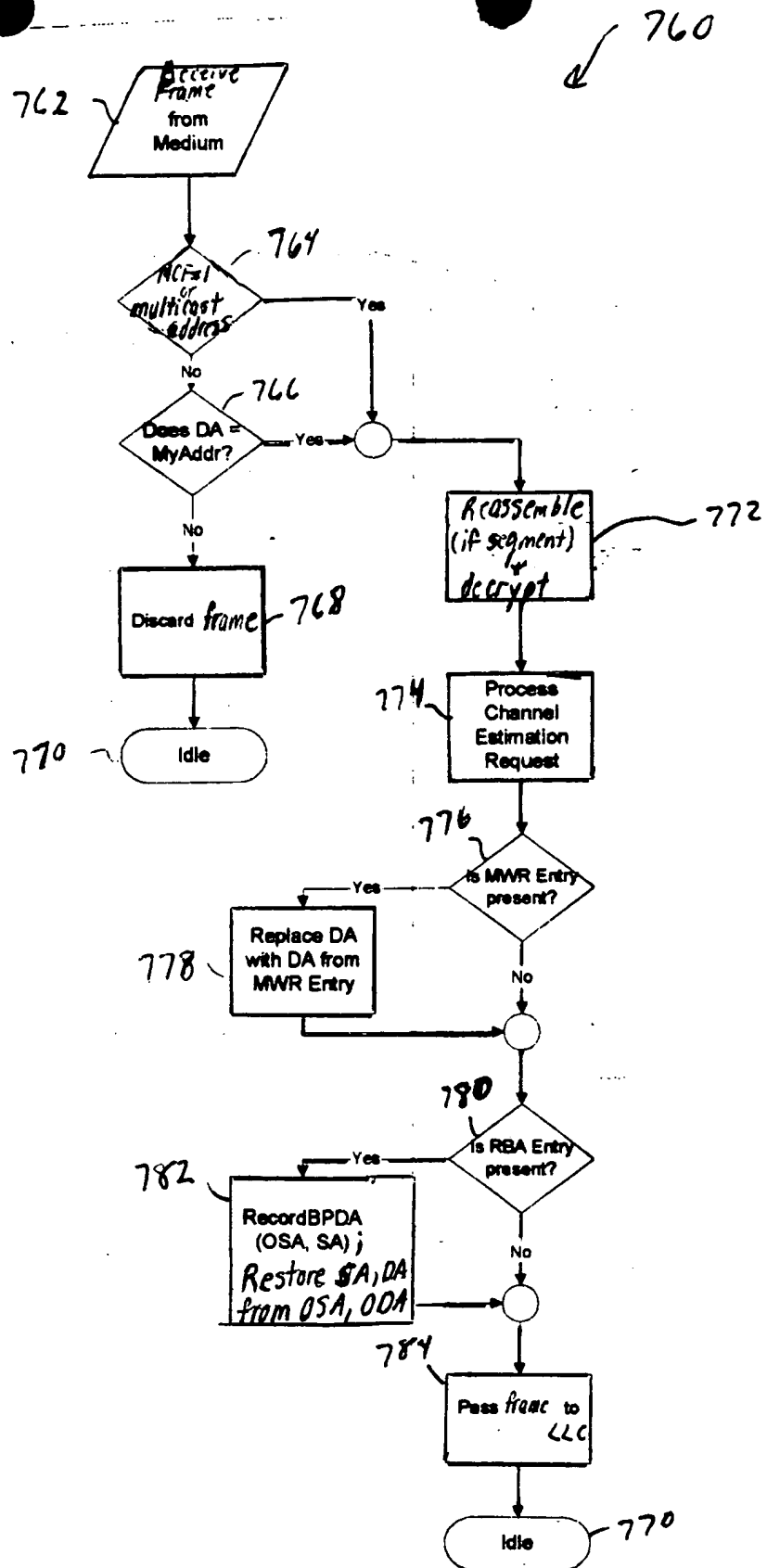


FIG. 36

700

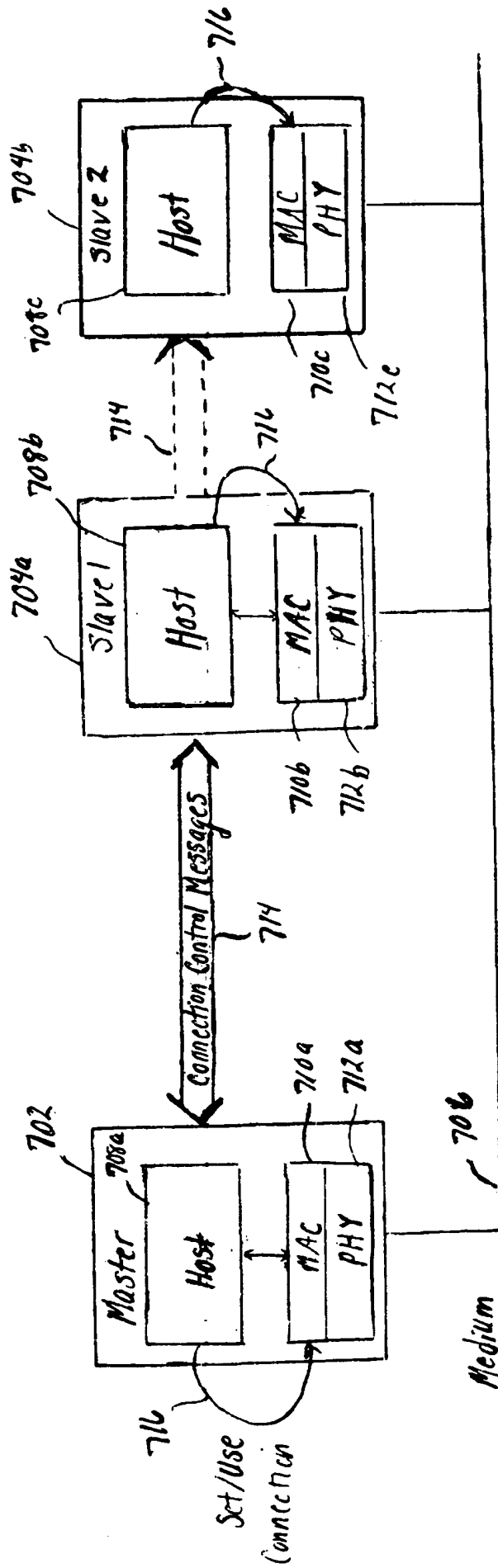


FIG. 37

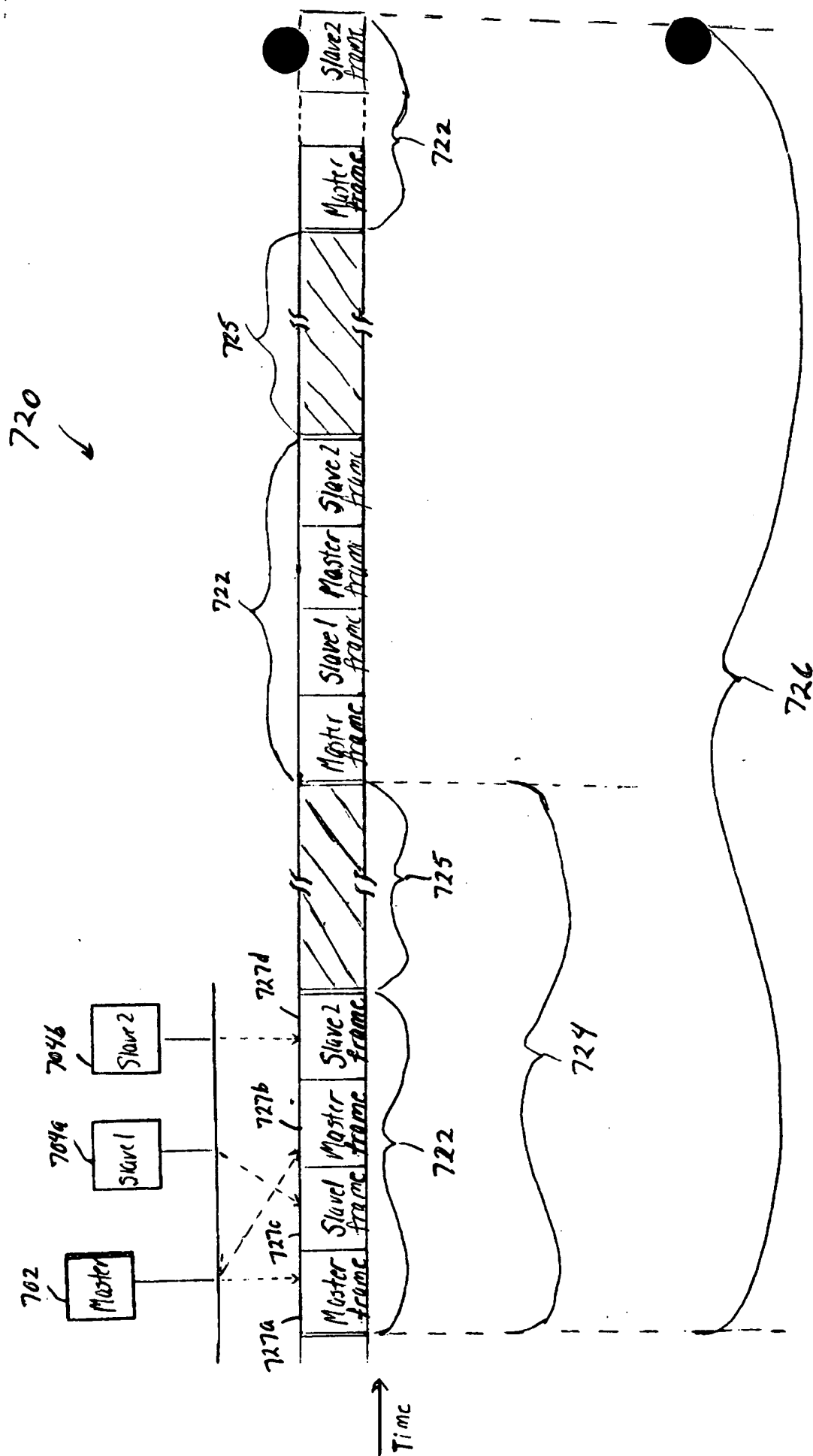
[illegible]

FIG. 38

740

744	746	748	750	754	756	752	760
Connection Number	Master	SA	SA Frame Size	Min Frame Time	Max Frame Time	TX Frame Size	Frame Life Control

FIG. 39A

742

Connection Number

FIG. 39B

800

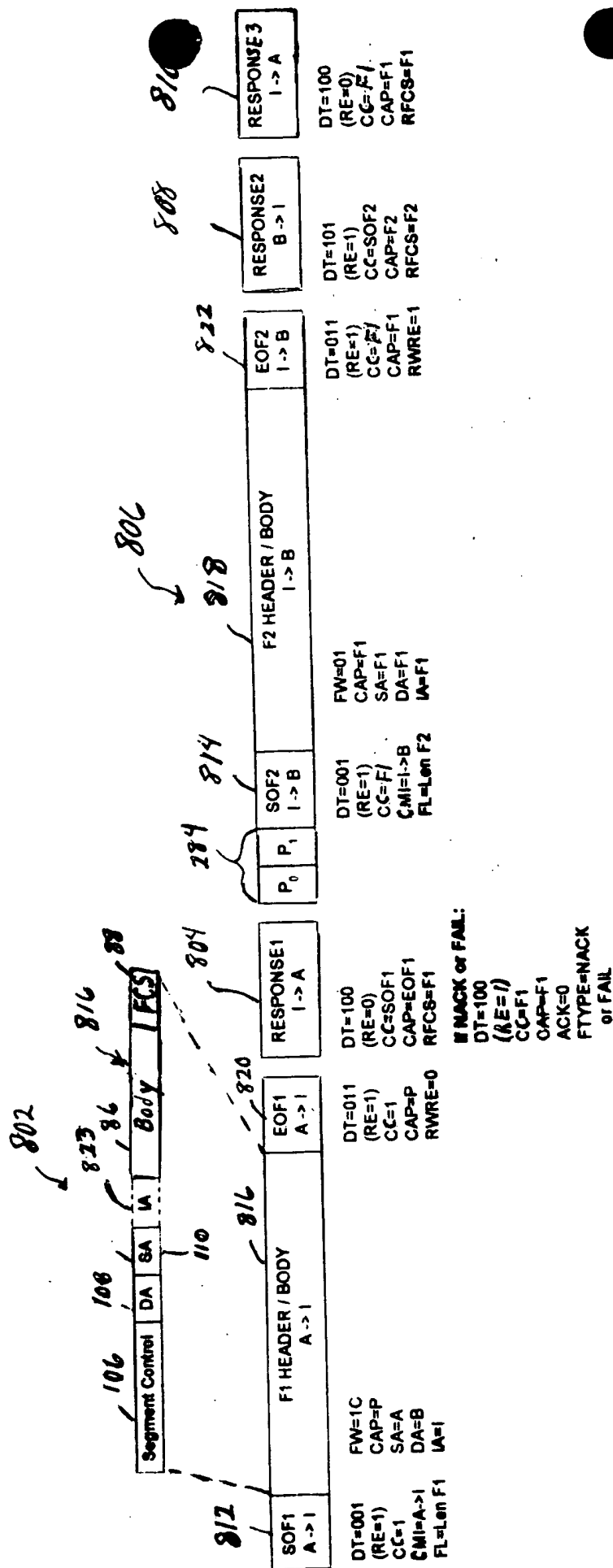


FIG. 40

824

802

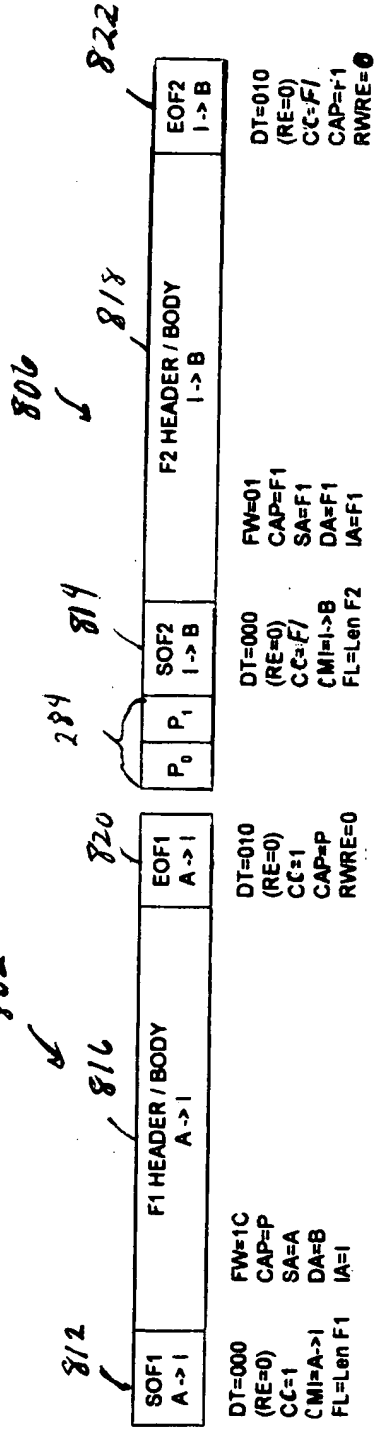


FIG. 41

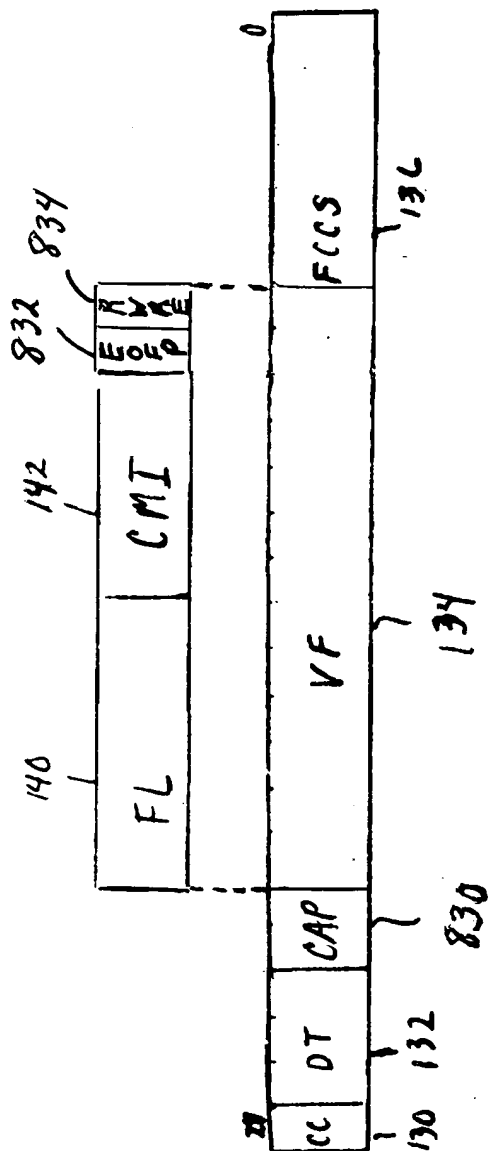


FIG. 42

836

802

806

812

SOF1 A->1	F1 HEADER / BODY A->1
--------------	--------------------------

DT=001
(RE=1)
CC=1
CMI=A->1
FL=Len F1
CAP=3
RWRE=1
EOFP=0

FW=1C
CAP=P
SA=A
DA=B
IA=1

816

814

SOF2 I->B	F2 HEADER / BODY I->B
--------------	--------------------------

DT=000
(RE=0)
CC=F1
CMI=I->B
FL=Len F2
CAP=F1
RWRE=1
EOFP=0

FW=01
CAP=F1
SA=A
DA=B
IA=1

818

808

RESPONSE2 B->1

DT=101
(RE=1)
CC=SO F2
CAP=F2
RFCS=F2

810

RESPONSE3 I->A

DT=100
(RE=0)
CC=F1
CAP=F1
RFCS=F1

FIG. 43

838

804

816

812



DT=001
(RE=1)
CC=1
CMI=A→I
FL=Len F1
CAP=3
RWRE=1
EOFP=0

FW=1C
CAP=P
SA=A
DA=B
IA=1

DT=100
(RE=0)
CC=F/
CAP=F1
ACK=0
FTYPE=NACK
or FAIL

FIG. 44

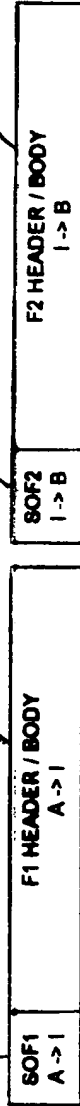
840

812

816

814

818



DT=001
(RE=1)
CC=1
CMI=A→I
FL=Len F1
CAP=3
RWRE=0
EOFP=0

FW=1C
CAP=P
SA=A
DA=B
IA=1

DT=000
(RE=0)
CC=F/
CMI=I→B
FL=Len F2
CAP=F1
RWRE=0
EOFP=0

FW=01
CAP=F1
SA=A
DA=B
IA=1

FIG. 45

102

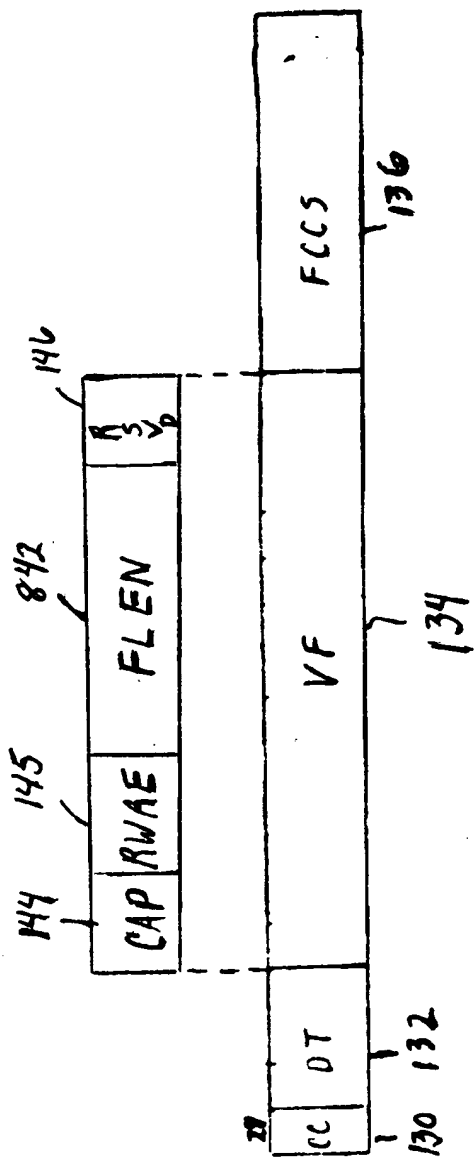


FIG. 46